

IN THE CLAIMS

Please amend Claims 1 and 6 and cancel Claims 4 and 8 as indicated:

1. (currently amended) An interface circuitry for connecting a data storage device to a computer, said interface circuitry comprising:

a parallel data line for transferring parallel data between the data storage device and the computer;

a multi-mode, parallel/serial data line for selectively transferring parallel or serial data between the data storage device and the computer;

a switching circuit, connected to said parallel/serial data line, having first and second selectable modes of operation, said switching circuit being connectable to the computer; and

a data transfer mode selection circuit connected to said switching circuit, said mode selection circuit for sending a parallel mode selection signal to said switching circuit for placing said switching circuit in said first mode of operation, wherein parallel data is transferable between the storage device and the computer over both the parallel data line and the parallel/serial data line, said mode selection circuit for sending a serial mode selection signal to said switching circuit for placing said switching circuit in said second mode of operation, wherein serial data is transferable between the storage device and the computer over said parallel/serial data line, and wherein the interface circuitry is connected to a docking station.

2. (original) The interface circuitry of claim 1, wherein the interface circuitry is electrically connected to an Advanced Technology Attachment/Integrated Drive Electronics (ATA/IDE) bus in the computer.

3. (original) The interface circuitry of claim 2, wherein the data transfer mode selection circuit is electrically connected to the switching circuit through the ATA/IDE bus.

4. (cancelled)

5. (original) The interface circuitry of claim 1, wherein the data transfer mode selection circuit is electrically connected to the switching circuit via a conductor, oriented between the computer and the data storage device, that is non-dedicated to the data transfer mode selection circuit.

6. (currently amended) A computer comprising:

a central processing complex (CPC) including a central processing unit, a memory and a memory and Input/Output (I/O) controller;

a data storage device;

an interface circuitry coupled between said data storage device and said CPC, said interface circuitry including:

a parallel data line for transferring parallel data between said data storage device and said CPC;

a multi-mode, parallel/serial data line for selectively transferring parallel or serial data between the data storage device and said CPC;

a switching circuit, connected to said parallel/serial data line, having first and second selectable modes of operation; and

a data transfer mode selection circuit connected to said switching circuit, said mode selection circuit for sending a parallel mode selection signal to said switching circuit for placing said switching circuit in said first mode of operation, wherein parallel data is transferable between the storage device and said CPC over both the parallel data line and the parallel/serial data line, said mode selection circuit for sending a serial mode selection signal to said switching circuit for placing said switching circuit in said second mode of operation, wherein serial data is transferable between said storage device and said CPC over said parallel /serial data line, and wherein the interface circuitry is connected to a docking station.

7. (original) The computer of claim 6, wherein the interface circuitry is electrically connected to an Advanced Technology Attachment/Integrated Drive Electronics bus.

8. (cancelled)

9. (original) The computer of claim 6, wherein the data transfer mode selection circuit is electrically connected to the switching circuit through the interface circuitry.

10. (original) The computer of claim 6, wherein the data transfer mode selection circuit is electrically connected to the switching circuit via a conductor, oriented between the computer and the data storage device, that is non-dedicated to the data transfer mode selection circuit.

11. (original) A docking station for use with a computer having a central processing complex (CPC) including a central processing unit, a memory and a memory and Input/Output (I/O) controller; a computer interface circuitry connectable between an external data storage device and said CPC, said computer interface circuitry including a parallel data line for transferring parallel data between said external data storage device and said CPC, a multi-mode, parallel/serial data line for selectively transferring parallel or serial data between said external data storage device and said CPC; a switching circuit, connected to said parallel/serial data line, having first and second selectable modes of operation, said docking station comprising:

a storage device interface connectable to said external data storage device, said storage device interface being connectable to said computer interface circuitry by a plurality of conductors; and

a data transfer mode selection circuit connectable to said switching circuit, said data transfer mode selection circuit for sending a parallel mode selection signal to said switching circuit for placing said switching circuit in said first mode of operation, wherein parallel data is transferable between said external data storage device and said CPC over both said parallel data line and said parallel/serial data line, said data transfer mode selection circuit for sending a serial mode selection signal to said switching circuit for placing said switching circuit in said second mode of operation, wherein serial data is transferable between said external data storage device and said CPC over said parallel /serial data line, wherein said docking station is capable of interfacing said external data storage device to said computer whether said external data storage device is a serial data storage device or a parallel data storage device.

12. (original) The docking station of claim 11, wherein said computer interface is electrically connected to an Advanced Technology Attachment/Integrated Drive Electronics bus in said computer.

13. (original) The docking station of claim 11, wherein said data transfer mode selection circuit is electrically connected to said switching circuit through said storage device interface.

14. (original) The docking station of claim 11, wherein said parallel data line is electrically connected to a Central Processor Unit- Input/Output (CPU-I/O) interface circuit of said computer via an Advanced Technology Attachment/Integrated Drive Electronics bus.

15. (original) The docking station of claim 11, wherein said serial data line is electrically connected to a Central Processor Unit-Input/Output (CPU-I/O) interface circuit of said computer via an Advanced Technology Attachment/Integrated Drive Electronics bus.

16. (original) The docking station of claim 11, wherein said data transfer mode selection circuit is electrically connected to said switching circuit via a conductor, from said plurality of conductors, that is non-dedicated to said data transfer mode selection circuit.